

## MICROELECTRONICS AND SEMICONDUCTORS

### Microelectronics and Semiconductors Funding<sup>1</sup>

(Dollars in Millions)

	FY 2022 Actual	Disaster Relief Supplemental			FY 2023 Estimate Total	FY 2024 Request
		FY 2023 Estimate Base	CHIPS and Base	Science		
BIO	\$3.00	-	-	-	-	-
CISE	41.00	40.00	-	-	40.00	40.00
EDU	-	-	-	-	-	6.00
ENG	43.00	43.00	-	-	43.00	63.00
MPS	50.96	31.00	-	-	31.00	33.00
TIP	15.00	25.69	12.56	11.99	50.24	67.68
<b>Total</b>	<b>\$152.96</b>	<b>\$139.69</b>	<b>\$12.56</b>	<b>\$11.99</b>	<b>\$164.24</b>	<b>\$209.68</b>

<sup>1</sup> Funding displayed may have overlap with other topics and programs.

### Overview

Without semiconductors, the world would be a very different place; we would not have cellphones, personal computers, electronically controlled cars, appliances, or many other technologies we rely upon every day. They are omnipresent in transportation, communications, healthcare, manufacturing, and information technology, among other industries. Yet, U.S.-led innovations in semiconductors and microelectronics have slowed in recent decades, and the Nation is now facing historically unprecedented global competition.

Transistors, the building blocks of today's microelectronics, are approaching fundamental down-scaling limits in both size and energy efficiency. Additionally, current transistor technologies require a significant investment of scarce natural resources to produce and are energy inefficient when used. Another important consideration is safety and security of microelectronics throughout the lifecycle. Microelectronics and semiconductors warrant strategic investment to ensure continued U.S. leadership in this foundational technology, which will also facilitate leadership in many other technology areas that underlie major sectors of the economy and critical aspects of national security. Investments in sustainable microelectronics—including holistic manufacturing processes that use environmentally-benign materials, encompass the entire manufacturing lifecycle, and account for energy efficiency, health and environmental impact, and cost effectiveness—are critical to the existential challenge of our generation: climate change.

The overarching objective of NSF's investment in Microelectronics and Semiconductors is to develop new paradigms in semiconductor capabilities. Ongoing activities and new, complementary opportunities will leverage and create advances in materials, devices, circuits, architectures, and related software and applications. Advances in microelectronics and semiconductors in recent years continue to be made through longstanding NSF programs, including Electronics, Photonics and Magnetic Devices; Communications, Circuits, and Sensing-Systems; Semiconductor Synthetic Biology; Foundations of Emerging Technologies; Software and Hardware Foundations; Principles and Practice

of Scalable Systems; Secure and Trustworthy Cyberspace; Advanced Manufacturing; Materials Research programs, including the cross-cutting Designing Materials to Revolutionize and Engineer our Future program; as well as Science and Technology Centers; Engineering Research Centers; and Industry-University Cooperative Research Centers. The new Designing for Environmental Sustainability in Computing program addresses environmental sustainability of computing through its entire lifecycle from design and manufacturing, through deployment into operation, and finally into reuse, recycling, and disposal. NSF programs for innovation and translation, including the NSF Partnerships for Innovation, NSF Innovation Corps (I-Corps™), and the Small Business Innovation Research and Small Business Technology Transfer programs, have enabled new knowledge and designs to make their way into the market and society.

NSF's investments aim to demonstrate sustainable new semiconductors and microelectronic devices capable of overcoming the looming natural limits of current technologies and architectures. These investments will also enable the training of a critically needed U.S. workforce capable of adapting and advancing these technologies for a broad range of societal needs. This approach to NSF's investment in Microelectronics and Semiconductors will help overcome scientific barriers in essential technologies such as: advanced computing; artificial intelligence; distributed mobile processing platforms; internet of things; quantum communication, computing, and sensing; advanced communications; advanced manufacturing; and biological-semiconductor interfaces.

## **Goals**

- *Support research and development of new, secure, high-performance devices and systems* supported by novel and sustainable materials that offer improved security and energy-efficient functionality.
- *Investigate and implement methods and techniques to integrate new classes of devices into microelectronic circuits for diverse platforms.* Microelectronic devices are fabricated by integrating transistors with numerous other components that work with different physical principles. The need to bring various components—electrical, optical, magnetic, and quantum—into a microelectronic circuit necessitates the investigation of new co-design, packaging, and testing methodologies.
- *Create a semiconductor and microelectronics R&D ecosystem.* This ecosystem will enable researchers and trainees to fabricate novel transistors and devices and to integrate component technologies into systems using heterogeneous integration techniques. The ecosystem will connect user facilities to fabricate devices in the laboratory, advanced methods for semiconductor manufacturing, and partnerships with industry to translate laboratory-generated ideas into foundry-fabricated prototypes.
- *Grow a diverse workforce* across the U.S. and provide experiential learning and training opportunities in partnership with industry to support the ecosystem, from researchers to technicians, theorists to experimentalists, and entrepreneurs to practitioners.

## **FY 2024 Investments**

### Research in Foundational Principles

Ongoing and new opportunities in foundational research, from individual-investigator projects to efforts comprising large multidisciplinary teams, will create new classes of novel, secure, sustainable, high-performance semiconductors for microelectronic devices.

Methods for Integrating Devices into Diverse Platforms

NSF will invest in existing and new opportunities, including research infrastructure and use-inspired research, to investigate and implement new methods for device integration and novel architectures.

Microelectronics Ecosystem

NSF will continue to invest in advanced manufacturing research, lab-to-fab opportunities, and research infrastructure, such as the NSF Quantum Foundries and NNCI, to translate benchtop microelectronics and semiconductors research to fabrication and manufacturing.

Workforce Development

To prepare a diverse microelectronics and semiconductors workforce across the U.S., NSF invests in STEM education at all levels and across settings. Relevant NSF programs include Advanced Technological Education, NSF Research Traineeship, Faculty Early Career Development, Research Experiences for Undergraduates, Research Experiences for Teachers, as well as semiconductor and microelectronics education in research projects. NSF support for Non-Academic Research Internships for Graduate Students and I-Corps™ provides students with industrial and entrepreneurship experience.

One example of an activity funded in FY 2024 is the expansion of the Future of Semiconductors (FuSe) program, which supports both research and workforce development. FuSe will invest in new materials, materials processing and characterization, fabrication, devices and systems, and computing, sensing, and communication systems answering both near-term supply chain concerns and longer-term Post-Moore's Law challenges. FuSe will invest in research opportunities in semiconductor-related areas including new materials, fabrication and manufacturing, electronics, and computer systems. FuSe will also consider research infrastructure needs in this domain, particularly improving semiconductor fabrication foundry access for NSF-funded researchers, and how potential partnerships with industry may facilitate such access.